

FIG.1A

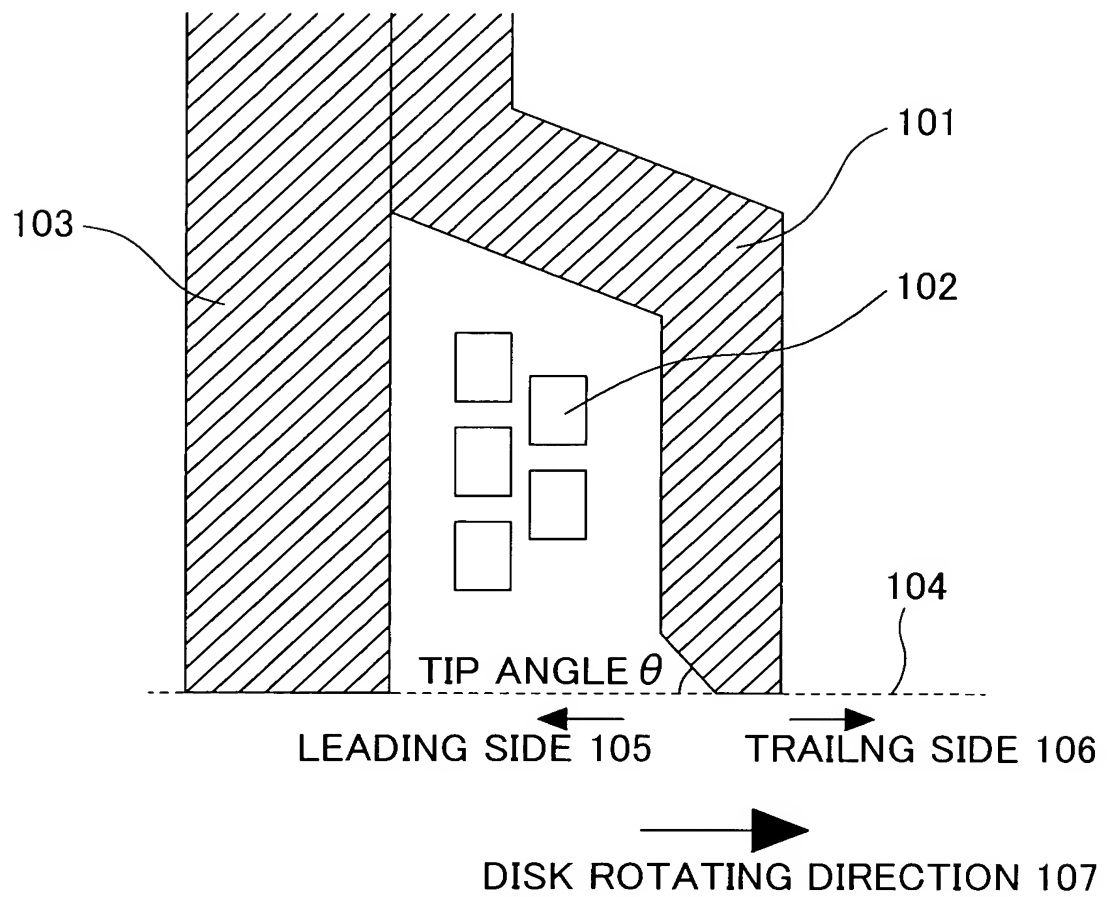


FIG.1B

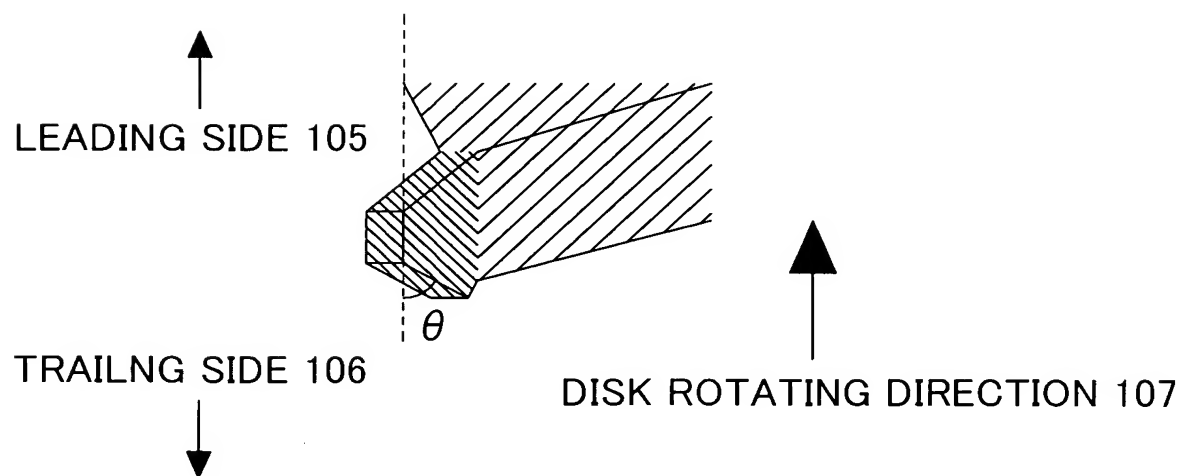


FIG.2

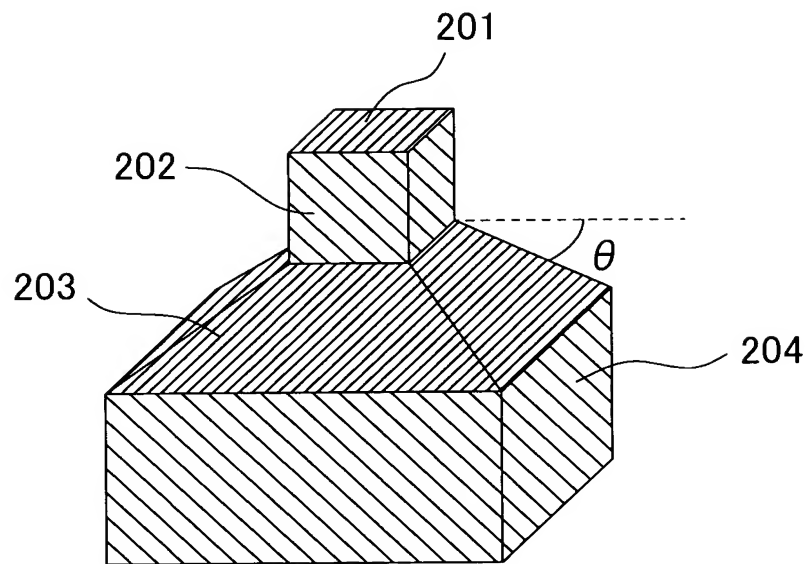


FIG.3A

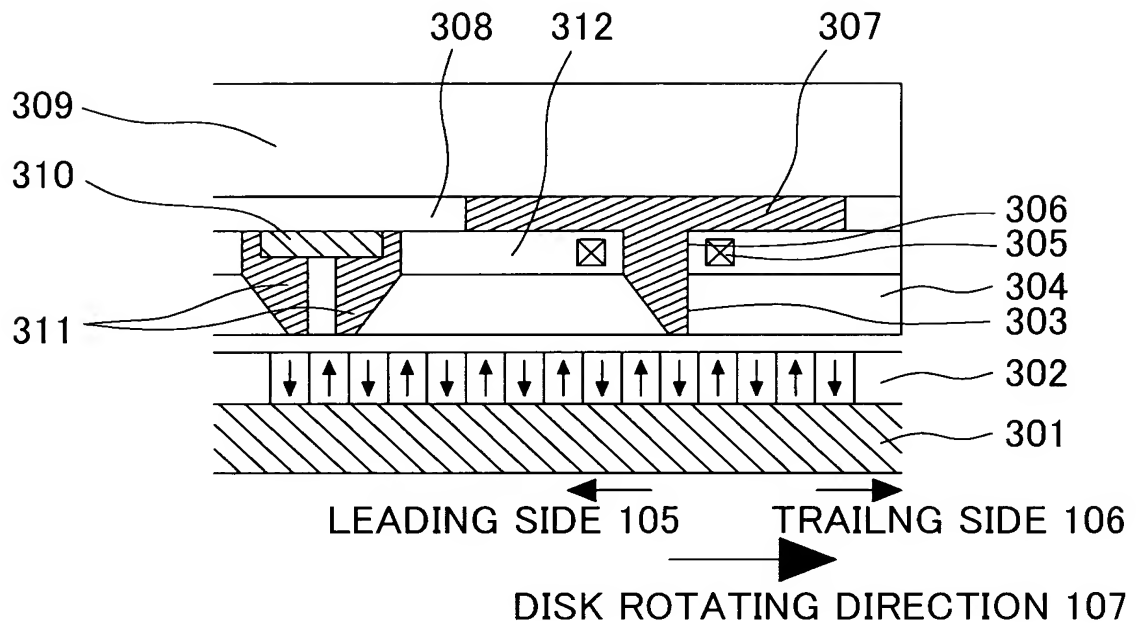


FIG.3B

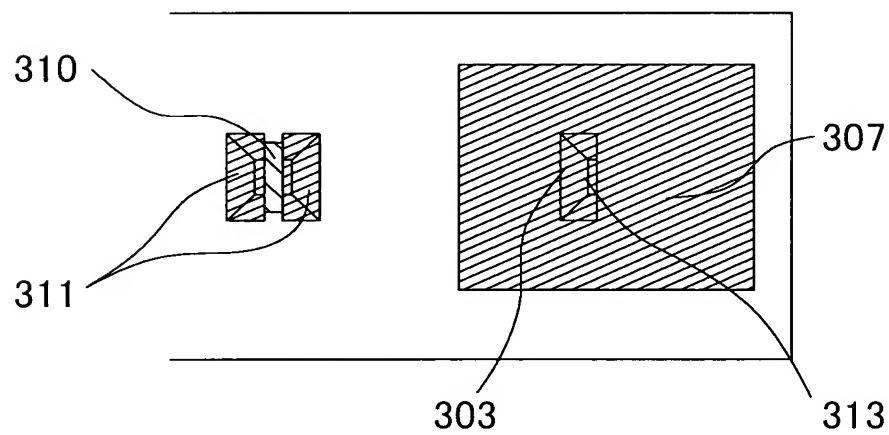


FIG.4A

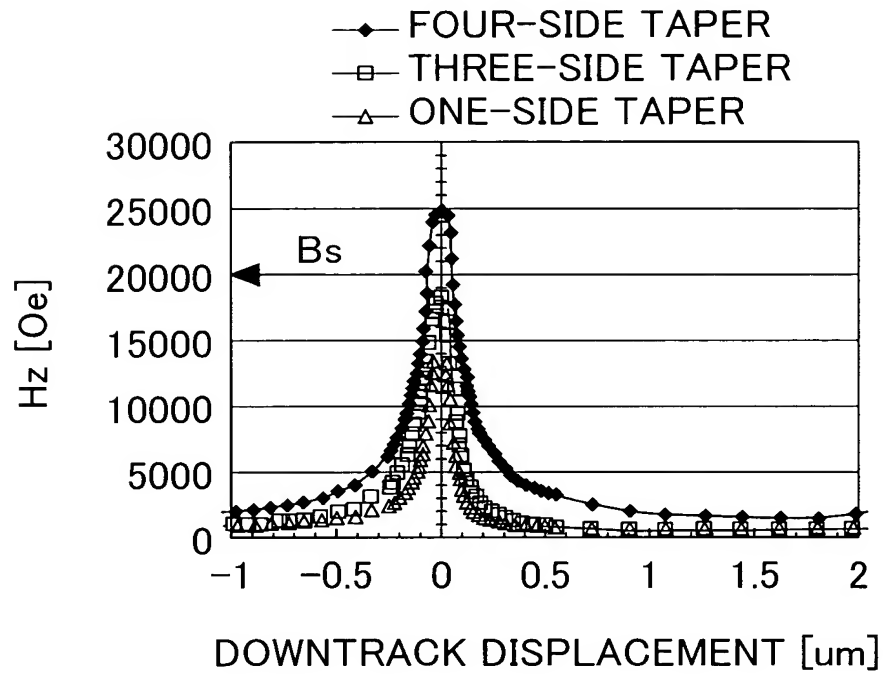


FIG.4B

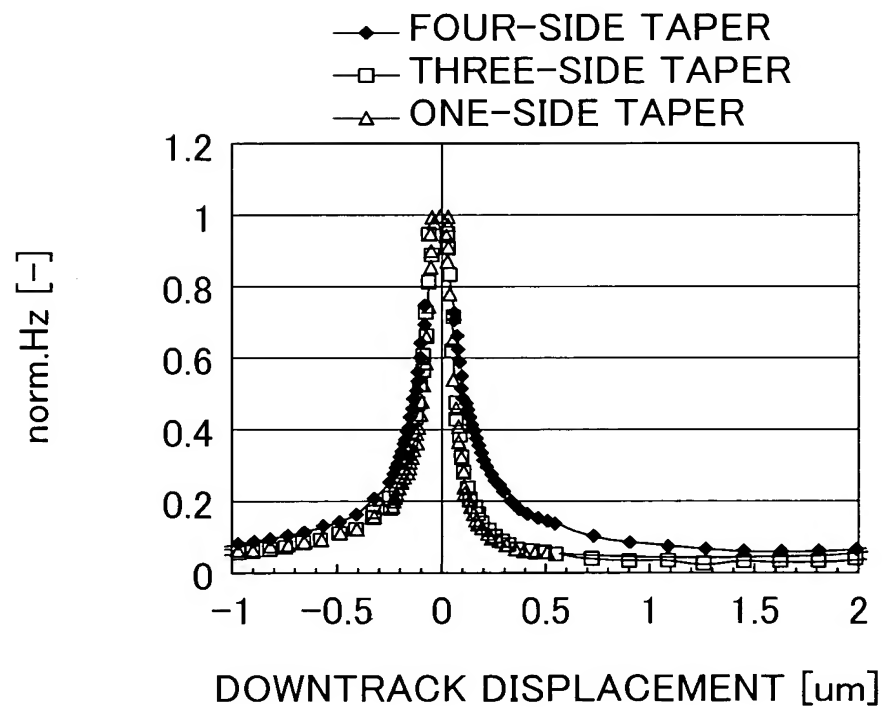


FIG.5

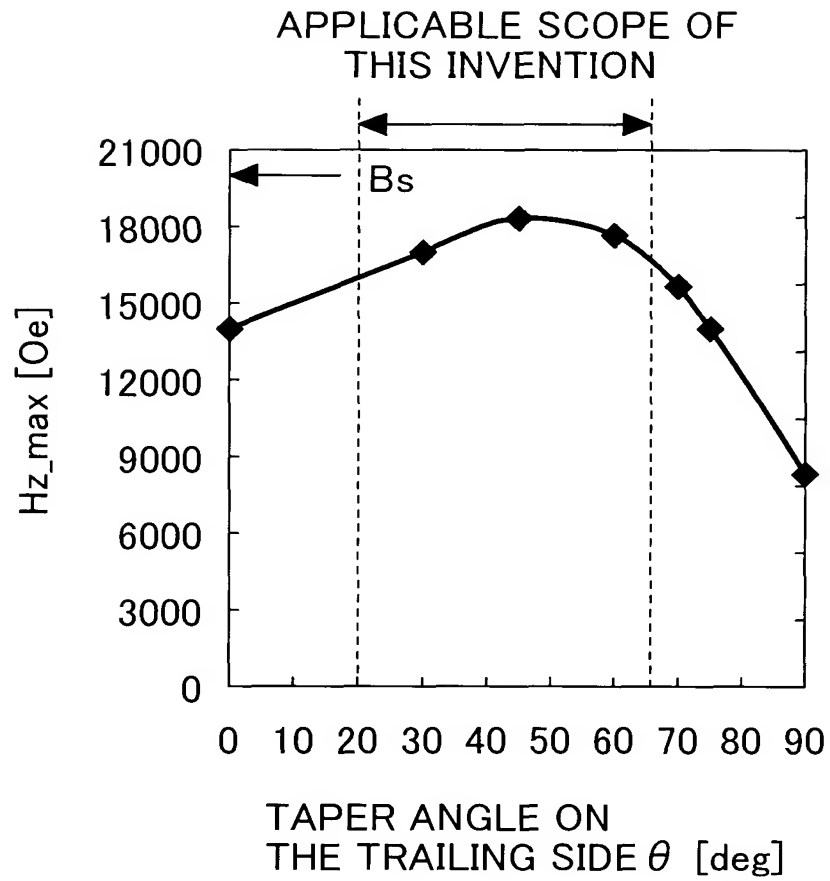


FIG.6

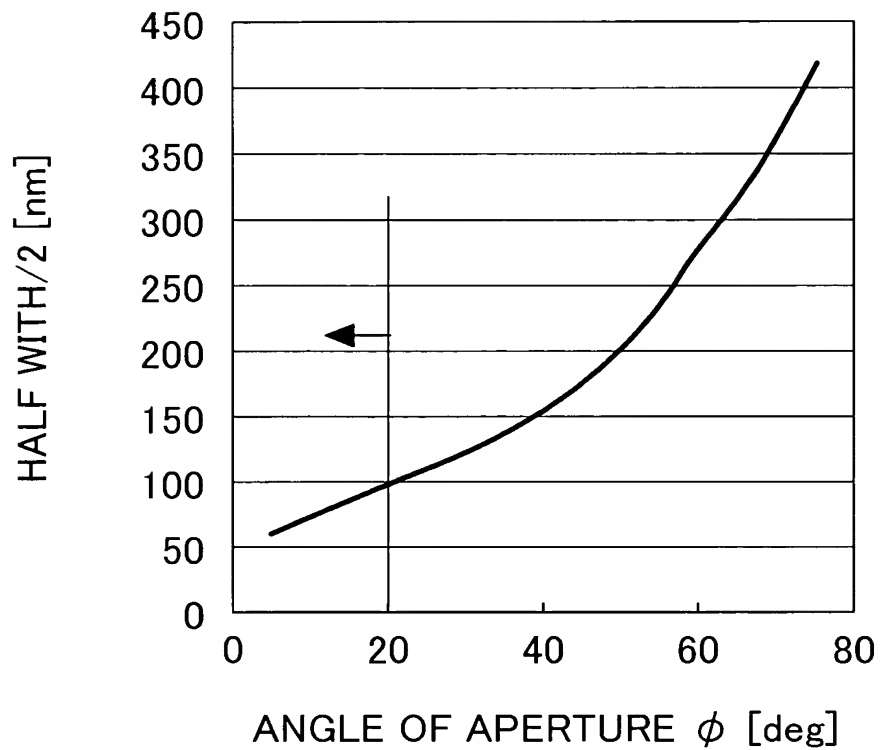


FIG.7

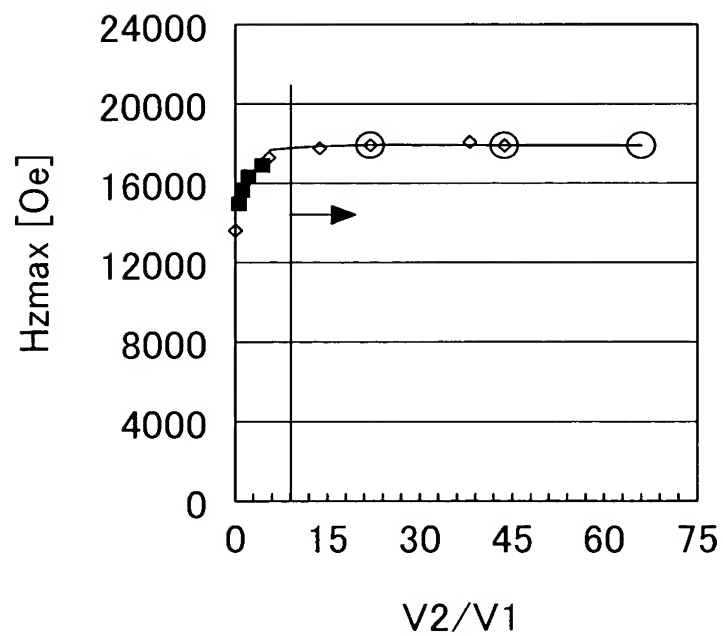


FIG.8A

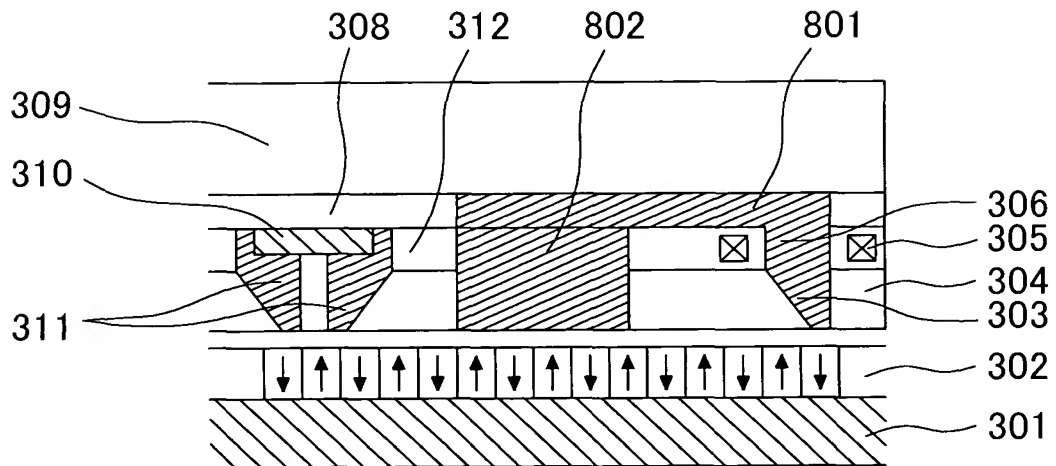


FIG.8B

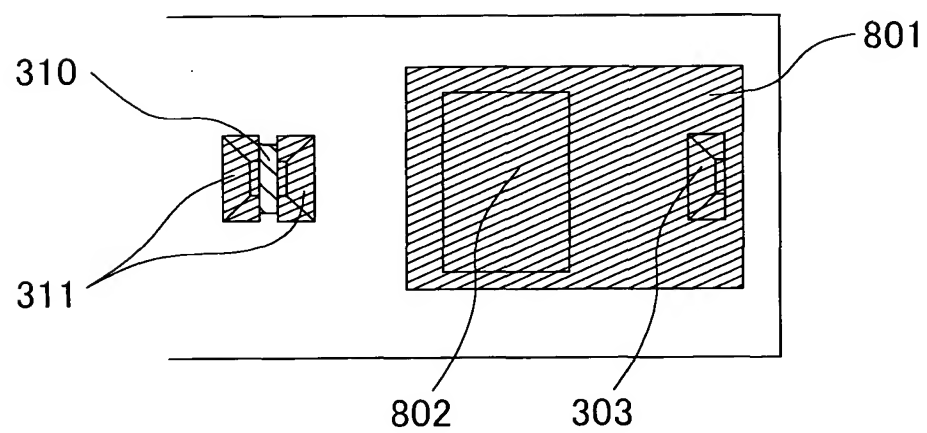


Figure 3 is a schematic diagram of a device 300. The device includes a substrate 303 with a central rectangular region 307. A component 310 is positioned on the left side of the substrate, and a component 901 is positioned on the right side of the substrate.



FIG.10A

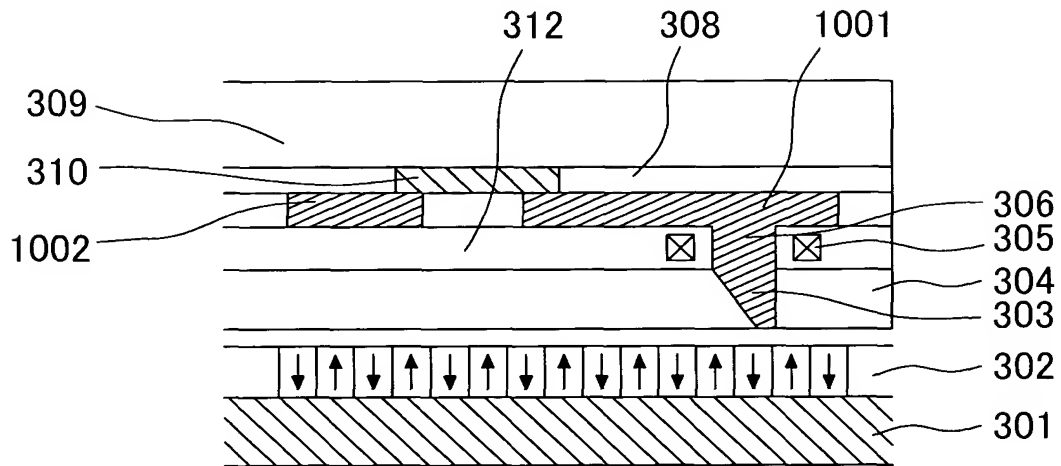


FIG.10B

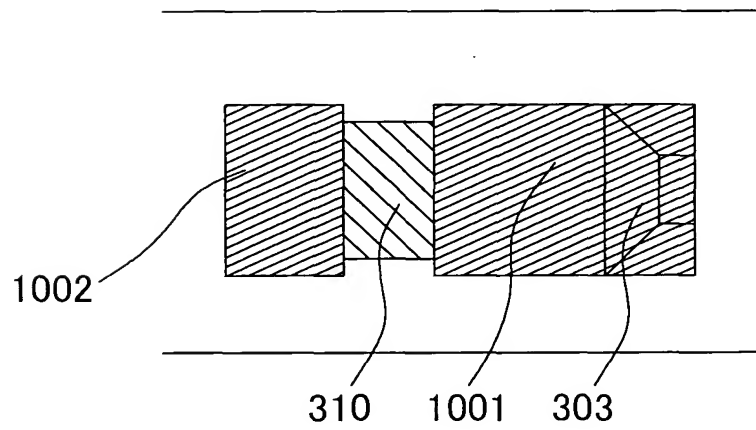


FIG.11A

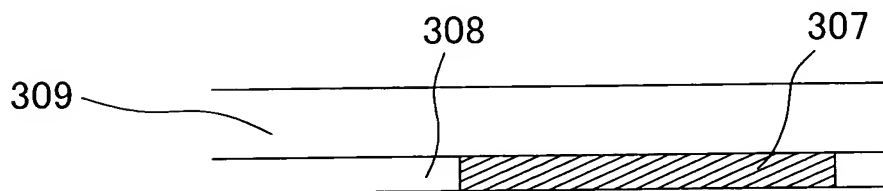


FIG.11B

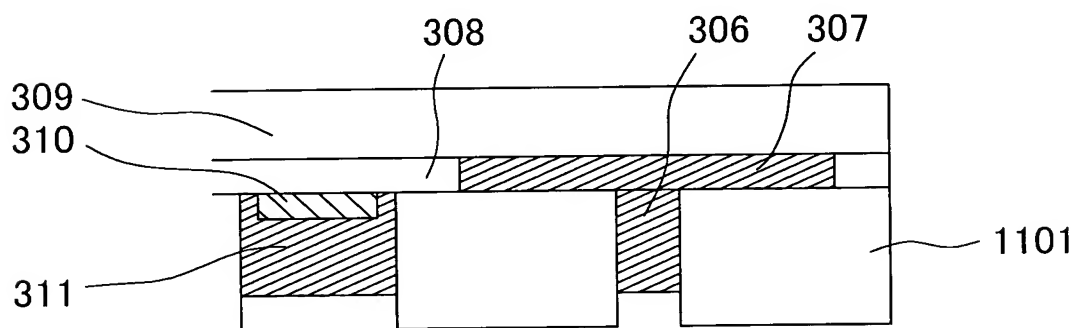


FIG.11C

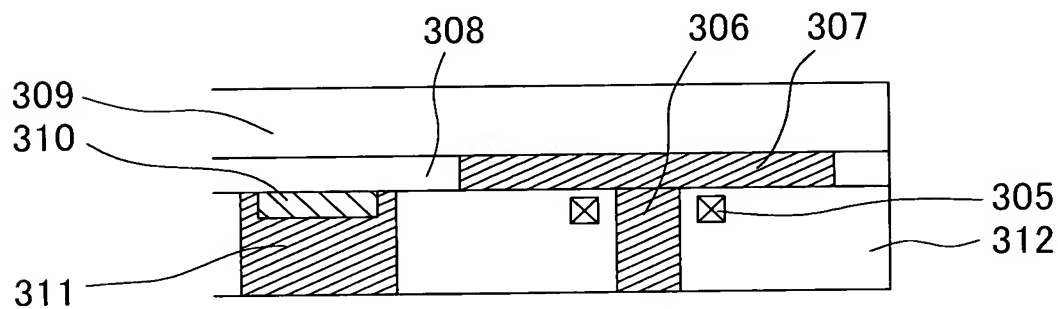


FIG.11D

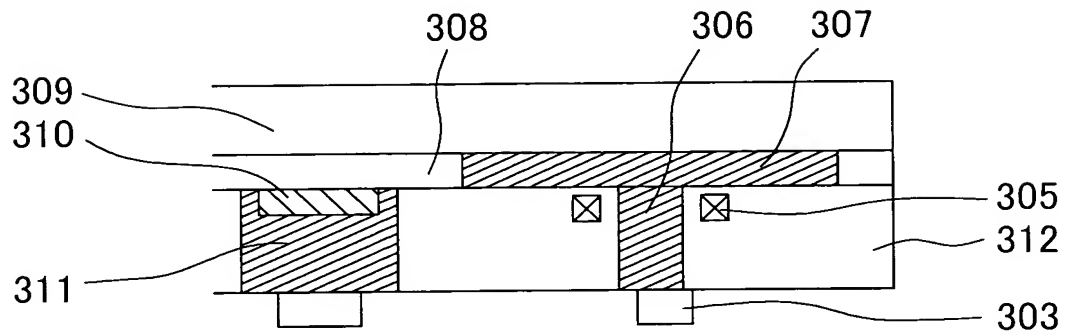


FIG.11E

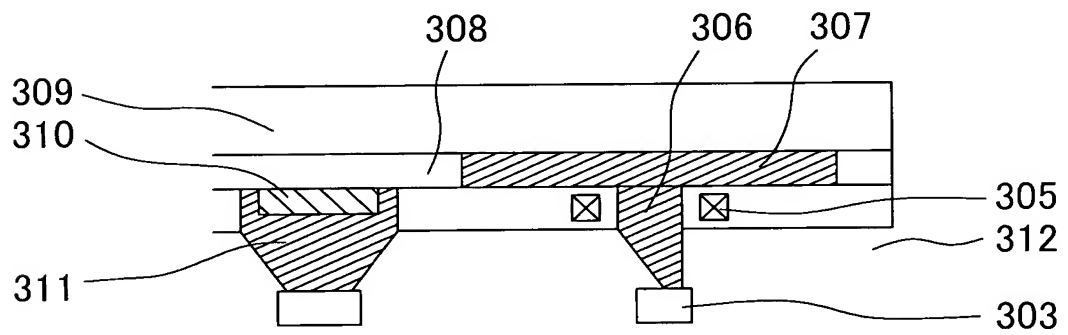


FIG.11F

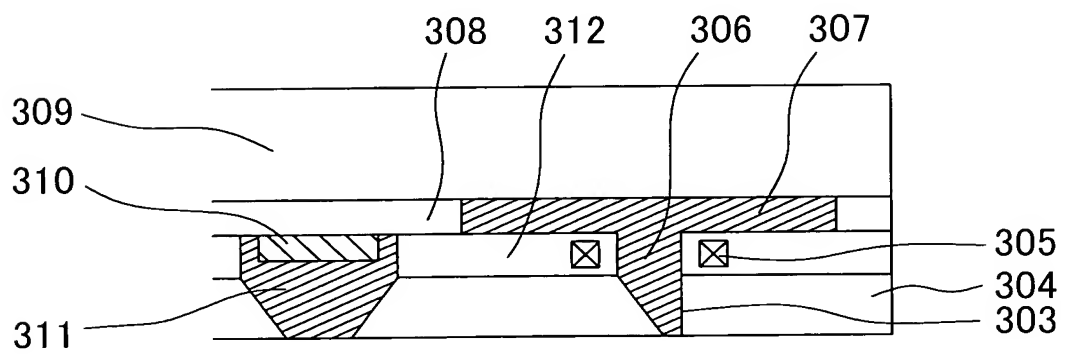


FIG.12A

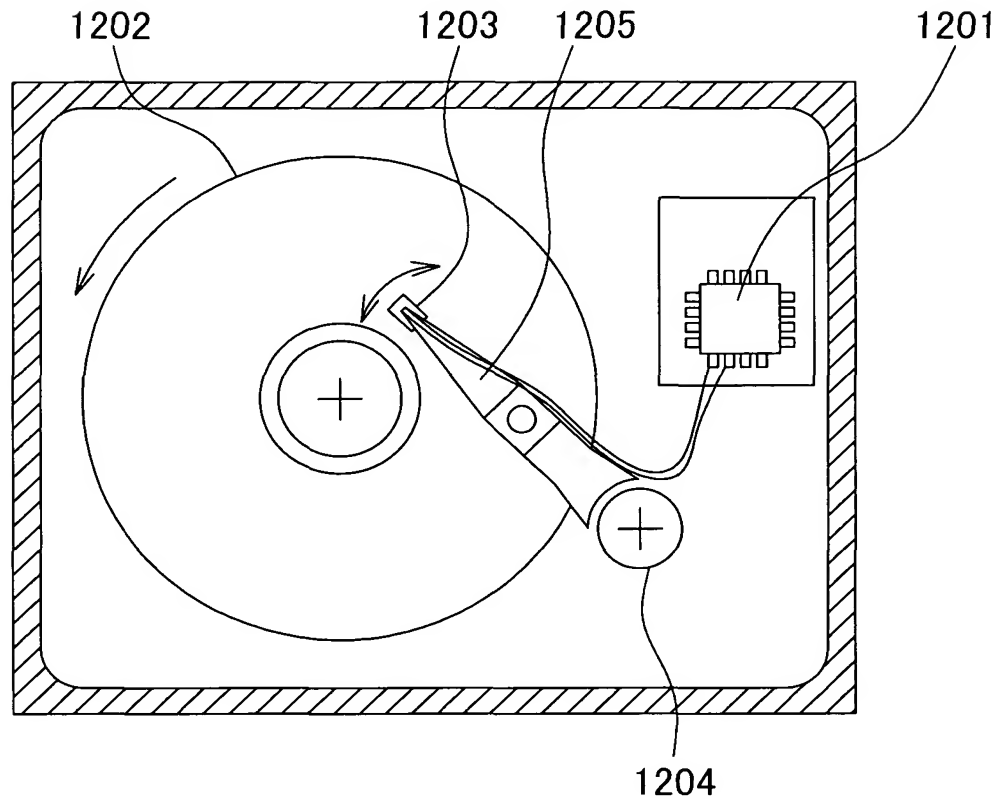


FIG.12B

